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Dkt. 2271/65888

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Yasutoshi HIRANO

Serial No.: 09/955,885

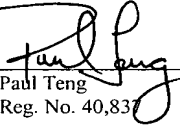
Group Art Unit: 2186

Date Filed: September 19, 2001

Examiner: Denise Tran

For: DIGITAL SIGNAL PROCESSOR AND MODEM USING THE SAME

I hereby certify that this correspondence is being deposited this date with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

 February 2, 2005
Paul Teng Date
Reg. No. 40,837

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Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

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AMENDMENT

Sir:

This Amendment is submitted in response to the Office Action dated November 10, 2004 in connection with the above-identified application.

Amendments to the Claims are reflected in the **Listing of Claims** section which begins on page 2.

Remarks begin on page 7 of this paper.